

S/N 09/820898

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Girish P. Ramanathan et al.

Examiner: Thuan Du

Serial No.: 09/820898

Group Art Unit: 2116

Filed: March 30, 2001

Docket No.: 884.963US1

Title: SYSTEM FOR VARYING TIMING BETWEEN SOURCE AND DATA  
SIGNALS IN A SOURCE SYNCHRONOUS INTERFACE

Assignee: Intel Corporation

Customer No: 21186

**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Technology Center 2100

This responds to the Office Action mailed on March 25, 2004. Please amend the above-identified patent application as follows.

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